AQ3045 Series Low Capacitance ESD Protection

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Pinout



Functional Block Diagram



Description

The AQ3045 includes back-to-back TVS diodes fabricated in a proprietary silicon avalanche technology to provide protection for electronic equipment that may experience destructive electrostatic discharges (ESD). These robust diodes can safely absorb repetitive ESD strikes up to the maximum level specified in IEC 61000-4-2 international standard (±30kV contact discharge) without performance degradation. The back-to-back configuration provides symmetrical ESD protection for data lines when AC signals are present and the low loading capacitance makes it ideal for protecting high speed data lines such as HDMI,USB2.0, USB3.0 and eSATA.

Features & Benefits

- ESD protection of ±30kV contact discharge, ±30kV air discharge, (IEC 61000-4-2)
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Lightning, 3A (8/20µs as defined in IEC 61000-4-5 2nd edition)
- Low capacitance of 0.35pF @ VR=0V (TYP)
- PPAP capable

Applications

- USB 3.0/USB 2.0/MHL
- MIPI Camera and Display
- HDMI 2.0, DisplayPort 1.3,
- eSATA Set Top Boxes, Game
- Consoles
- Smart Phones

- Low leakage current of 100nA at 5.3V (MAX)
- Small SOD882 packaging helps save board space
- Extremely low dynamic resistance (0.55 Ω TYP)
- AEC-Q101 qualified
- Halogen free, lead free and RoHS compliant
- Moisture Sensitivity Level(MSL -1)
- External Storage
- Ultrabooks, Notebooks
- Tablets, eReaders
- High Speed Serial Interfaces
- Automotive applications

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.



Absolute Maximum Ratings

Symbol Parameter		Value	Units
P _{PK}	Peak Pulse Power ($t_p=8/20\mu s$)	40	W
۱ _{PP}	Peak Current (t _p =8/20µs)	3.0	А
T _{op}	Operating Temperature	-40 to 150	°C
T _{STOR}	Storage Temperature	-55 to 150	°C

Caution: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics (T_{op}=25°C)

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Units
Reverse Standoff Voltage	V _{RWM}	I _R =1uA	-	-	5.3	V
Breakdown Voltage	V _{BR}	I _R =1mA	6.8	7.8	9.0	V
Reverse Leakage Current	I _{LEAK}	V _R =5.3V	-	<10	100	nA
Clamp Voltage ¹	V _c	I _{PP} =1A, t _p =8/20μs	-	-	12.0	V
Dynamic Resistance ²	R _{DYN}	TLP, t_p =100ns, I/O to GND	-	0.55	-	Ω
ESD Withstand Voltage ¹	1/	IEC 61000-4-2 (Contact)	±30	-	-	kV
	V _{esd}	IEC 61000-4-2 (Air)	±30	-	-	kV
Diode Capacitance ¹	C _{I/O-I/O}	Reverse Bias=0V, f=1MHz	-	0.35	0.5	pF

Note:

¹ Parameter is guaranteed by design and/or component characterization.
² Transmission Line Pulse (TLP) with 100ns width, 2ns rise time, and average window t1=70ns to t2= 90ns



8/20µs Pulse Waveform

Transmission Line Pulsing(TLP) Plot



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Clamping Voltage vs IPP



Capacitance vs. Reverse Bias



Soldering Parameters

Reflow Condition		Pb – Free assembly	
Pre Heat	- Temperature Min (T _{s(min)})	150°C	
	- Temperature Max (T _{s(max)})	200°C	
	- Time (min to max) (t _s)	60 - 120 secs	
Average ramp up rate (Liquidus) Temp (T_L) to peak		3°C/second max	
T _{S(max)} to T _L - Ramp-up Rate		3°C/second max	
Reflow	- Temperature (T _L) (Liquidus)	217°C	
nellow	- Temperature (t _L)	60 – 150 seconds	
Peak Temperature (T _P)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature $(t_{\rm p})$		30 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peak Temperature (T _p)		8 minutes Max.	
Do not exceed		260°C	

Part Numbering System





Product Characteristics

Lead Plating	Pre-Plated Frame
Lead Material	Copper Alloy
Substrate material	Silicon
Body Material	Molded Compound
Flammability	UL Recognized compound meeting flammability rating V-0

Part Marking System



Ordering Information

Part Number	Package	Min. Order Qty.
AQ3045-01ETG	SOD882	10000



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Package Dimensions – SOD882





	Symbol	DIMENSIONS (mm)			
	Symbol	Min.	Nor.	Max.	
	А	0.36	0.45	0.55	
A1 0.127 REF					
	L1	0.20	0.25	0.30	
	L2	0.45	0.50	0.55	
	D	0.93	1.00	1.07	
	E	0.53	0.60	0.67	
	е	0.65 BSC			
	h	0.07	0.12	0.17	

Recommended Soldering Pad Layout





Embossed Carrier Tape & Reel Specification - SOD882



Cumula al	Millimetres		Inches	
Symbol	Min	Мах	Min	Max
А	0.65	0.70	0.026	0.028
В	1.10	1.20	0.043	0.047
С	0.50	0.60	0.020	0.024
dØ	1.40	1.60	0.055	0.063
E	1.65	1.85	0.065	0.073
F	3.40	3.60	0.134	0.142
P0	3.90	4.10	0.154	0.161
Р	1.90	2.10	0.075	0.083
P1	1.90	2.10	0.075	0.083
W	7.90	8.10	0.311	0.319

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