

APPROVAL SHEET

MULTILAYER CERAMIC CAPACITORS

General Purpose Series (4V to 100V)

0201 to 1812 Sizes

NP0, X7R, Y5V, X6S, X7S & X5R Dielectrics

Halogen Free & RoHS Compliance



*Contents in this sheet are subject to change without prior notice.

Multilayer Ceramic Capacitors

1. DESCRIPTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

WTC's MLCC is made by NP0, X7R, X6S, X5R and Y5V dielectric material and which provides product with high electrical precision, stability and reliability.

2. FEATURES

- a. A wide selection of sizes is available (0201 to 1812).
- b. High capacitance in given case size.
- c. Capacitor with lead-free termination (pure Tin).

3. APPLICATIONS

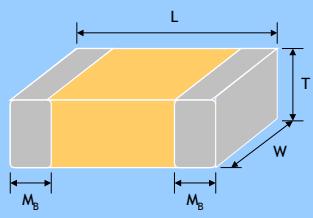
- a. For general digital circuit.
- b. For power supply bypass capacitors.
- c. For consumer electronics.
- d. For telecommunication.

4. HOW TO ORDER

1206	B	104	K	500	C	I
Size Inch (mm) 0201 (0603) 0402 (1005) 0603 (1608) 0805 (2012) 1206 (3216) 1210 (3225) 1812 (4532)	Dielectric N=NP0 (C0G) B=X7R F=Y5V X=X5R S=X6S A=X7S	Capacitance Two significant digits followed by no. of zeros. And R is in place of decimal point. eg.: 0R5=0.5pF 1R0=1.0pF 104=10x10 ⁴ =100nF	Tolerance A=±0.05pF B=±0.1pF C=±0.25pF D=±0.5pF F=±1% G=±2% J=±5% K=±10% M=±20% Z=−20/+80%	Rated voltage Two significant digits followed by no. of zeros. And R is in place of decimal point. 4R0=4 VDC 6R3=6.3 VDC 100=10 VDC 160=16 VDC 250=25 VDC 500=50 VDC 101=100 VDC	Termination C=Cu/Ni/Sn	Packaging style T=7" reeled G=13" reeled

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5. EXTERNAL DIMENSIONS

Outline	Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol	Soldering Method *	M _B (mm)
 Fig. 1 The outline of MLCC	01R5 (0402)	0.4±0.02	0.2±0.02	0.2±0.02	V	0.10±0.03
	0201 (0603)	0.6±0.03	0.3±0.03	0.3±0.03	R	0.15±0.05
		0.6±0.05 ^{#2}	0.3±0.05 ^{#2}	0.3±0.05 ^{#2}		0.15+0.1/-0.05
		0.6±0.09 ^{#3}	0.3±0.09 ^{#3}	0.3±0.09 ^{#3}		
	0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	0.25
				0.50+0.02/-0.05	Q	+0.05/-0.10
		1.00±0.20	0.50±0.20	0.5±0.20	E	R
	0603 (1608)	1.60±0.10	0.80±0.10	0.80±0.07	S	R / W
		1.60+0.15/-0.10	0.80+0.15/-0.10	0.50±0.10	H	
		1.60±0.20 ^{#1}	0.80±0.20 ^{#1}	0.80+0.15/-0.10	X	
				0.8±0.20 ^{#1}		
	0805 (2012)	2.00±0.15	1.25±0.10	0.50±0.10	H	R / W
				0.60±0.10	A	
				0.80±0.10	B	
				1.25±0.10	D	
				0.85±0.10	T	
		2.00±0.20	1.25±0.20	1.25±0.20	I	R
	1206 (3216)	3.20±0.15	1.60±0.15	0.80±0.10	B	R / W
				0.95±0.10	C	
				1.25±0.10	D	
				1.15±0.15	J	
		3.20±0.20	1.60±0.20	1.60±0.20	G	R
				0.85±0.10	T	
				1.60+0.30/-0.10	P	
				1.60+0.30/-0.10	R	
	1210 (3225)	3.20±0.30	2.50±0.20	0.95±0.10	C	R
				0.85±0.10	T	
				1.25±0.10	D	
		3.20±0.40	2.50±0.30	1.60±0.20	G	R
				2.00±0.20	K	
				2.50±0.30	M	
	1808 (4520)	4.50±0.40 (4.5+0.5/-0.3)**	2.03±0.25	2.50±0.50 ^{#4}	2.50±0.50 ^{#4}	R
				1.25±0.10	D	
				1.40±0.15	F	
				1.60±0.20	G	
		4.50±0.40 (4.5+0.5/-0.3)**	3.20±0.40	2.00±0.20	K	R
				2.50±0.30	M	
	1812 (4532)	4.50±0.40 (4.5+0.5/-0.3)**	3.20±0.30	2.80±0.30	U	R
				1.25±0.10	D	
				1.60±0.20	G	
				2.00±0.20	K	
		4.50±0.40 (4.5+0.5/-0.3)**	3.20±0.40	2.50±0.30	M	R
				2.80±0.30	U	

* R = Reflow soldering process ; W = Wave soldering process.

** For 1808_200V ~3kV, 1812_200V~3kV and safety certificated products.

*** For 1206_1000V ~3kV, 1808_200V ~3kV, 1812_200V~3kV and safety certificated products.

#1 : For 0603/Cap≥10μF or 0603/Cap≥4.7μF(≤6.3V) or 0603/Cap>1μF(>10V) products.

#2 : For 0201/ 0.1uF < Cap < 0.68uF products.

#3 : For 0201/Cap≥0.68μF products.

#4 : For 1210_100V: Cap > 1μF, 250V: Cap >0.47μF, 400V~630V: Cap >0.22μF.

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6. GENERAL ELECTRICAL DATA

Dielectric	NP0	X7R	Y5V	X5R	X6S	X7S
Size	0201, 0402, 0603, 0805, 1206, 1210, 1812					
Capacitance range*	0.1pF to 0.1μF	100pF to 47μF	0.01μF to 100μF	100pF to 220μF	0.1μF to 100μF	1μF to 100μF
Capacitance tolerance**	Caps≤5pF ^{#1} : A (±0.05pF), B (±0.1pF), C (±0.25pF) 5pF<Cap<10pF: C (±0.25pF), D (±0.5pF) Cap≥10pF: F (±1%), G (±2%), J (±5%), K (±10%)	J (±5%), K (±10%), M (±20%)	M (±20%), Z (-20/+80%)	K (±10%), M (±20%)	K (±10%), M (±20%)	K (±10%), M (±20%)
Rated voltage (WVDC)	10V, 16V, 25V, 50V, 100V	6.3V, 10V, 16V, 25V, 50V, 100V				
DF(Tan δ)*	Cap<30pF: Q≥400+20C Cap≥30pF: Q≥1000	Note 1				
Operating temperature	-55 to +125°C		-25 to +85°C	-55 to +85°C	-55 to +105°C	-55 to +125°C
Capacitance characteristic	±30ppm	±15%	+30/-80%	±15%	±22%	±22%
Termination	Ni/Sn (lead-free termination)					

#1: NP0, 0.1pF product only provide B tolerance; 0603N0R4 provide B&C tolerance; 0603N0R3 only provide C tolerance.

* Measured at the condition of 30~70% related humidity.

NP0: Apply 1.0±0.2Vrms, 1.0MHz±10% for Cap≤1000pF and 1.0±0.2Vrms, 1.0kHz±10% for Cap>1000pF, 25°C at ambient temperature

X7R/X6S/X5R/X7S: Please refer to page 13 "Reliability test conditions and requirements" for detail.

Y5V: Apply 1.0±0.2Vrms, 1.0kHz±10%, at 20°C ambient temperature.

** Preconditioning for Class II MLCC: Perform a heat treatment at 150±10°C for 1 hour and then leave in ambient condition for 24±2 hours before measurement.

Note 1:

X7R/X5R/X6S/X7S

Rated Vol.	D.F. ≤	Exception of D.F. ≤
≥ 100V	≤ 2.5%	≤ 3% 1206 ≥ 0.47μF ≤ 5% 0805 > 0.1μF; 0603 ≥ 0.068μF; 1206 > 1μF; 1210 ≥ 2.2μF; TT series ≤ 10% 0805 > 0.22μF; 1210 ≥ 3.3μF
	50V	≤ 3% 0201(50V); 0603 ≥ 0.047μF; 0805 ≥ 0.18μF; 1206 ≥ 0.47μF ≤ 5% 0201 ≥ 0.01μF; 1210 ≥ 4.7μF ≤ 10% 0402 ≥ 0.012μF; 0603 > 0.1μF; 0805 ≥ 1μF; 1206 ≥ 2.2μF; 1210 ≥ 10μF; TT series
		≤ 12.5% 0201 ≥ 0.01μF; 0805 ≥ 1μF; 1210 ≥ 10μF ≤ 7% 0603 ≥ 0.33μF; 1206 ≥ 4.7μF ≤ 10% 0201 ≥ 0.1μF; 0402 ≥ 0.10μF & (0402/X7R ≥ 0.056μF); TT series 0603 ≥ 0.47μF; 0805 ≥ 2.2μF; 1206 ≥ 6.8μF; 1210 ≥ 22μF ≤ 12.5% 0402 ≥ 0.47μF
25V	≤ 3.5%	≤ 5% 0201 ≥ 0.01μF; 0402 ≥ 0.033μF; 0603 ≥ 0.15μF; 0805 ≥ 0.68μF; 1206 ≥ 2.2μF; 1210 ≥ 4.7μF ≤ 10% 0201 ≥ 0.1μF (0201/X7R ≥ 0.022μF); 0402 ≥ 0.22μF; 0603 ≥ 0.68μF; 0805 ≥ 2.2μF; 1206 ≥ 4.7μF; 1210 ≥ 22μF; TT series
		≤ 15% 0201 ≥ 0.012μF; 0402 ≥ 0.33μF (0402/X7R ≥ 0.22μF); TT series 0603 ≥ 0.33μF; 0805 ≥ 2.2μF; 1206 ≥ 2.2μF; 1210 ≥ 22μF; 01R5
		≤ 20% 0201 ≥ 0.1μF; 0402 ≥ 1μF
		≤ 15% 0201 ≥ 0.1μF; 0402 ≥ 1μF; 0603 ≥ 10μF; 0805 ≥ 4.7μF; 1206 ≥ 47μF; 1210 ≥ 100μF; TT series ≤ 20% 0402 ≥ 2.2μF
4V	≤ 15%	---

Rated vol.	D.F. ≤	Exception of D.F. ≤
≥ 50V	≤ 5%	≤ 7% 0603 ≥ 0.1μF; 0805 ≥ 0.47μF; 1206 ≥ 4.7μF; TT series ≤ 12.5% 1210 ≥ 6.8μF
	≤ 7%	---
25V	≤ 5%	≤ 7% 0402 ≥ 0.047μF; 0603 ≥ 0.1μF; 0805 ≥ 0.33μF; 1206 ≥ 1μF; 1210 ≥ 4.7μF ≤ 9% 0402 ≥ 0.068μF; 0603 ≥ 0.47μF; 1206 ≥ 4.7μF; 1210 ≥ 22μF; TT series
	≤ 7%	≤ 9% 0402 ≥ 0.068μF; 0603 ≥ 0.68μF ≤ 12.5% 0402 ≥ 0.22μF
16V (C < 1.0μF)	≤ 9%	≤ 12.5% 0603 ≥ 2.2μF; 0805 ≥ 3.3μF; 1206 ≥ 10μF; 1210 ≥ 22μF; 1812 ≥ 47μF; TT series
	≤ 12.5%	≤ 20% 0402 ≥ 0.47μF
6.3V	≤ 20%	---

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7-1. NP0 Dielectric 1206, 1210, 1812 Sizes

DIELECTRIC		NP0													
SIZE		1206					1210					1812			
RATED VOLTAGE (VDC)		10	16	25	50	100	10	16	25	50	100	16	25	50	100
Capacitance	1.0pF (1R0)														
	1.2pF (1R2)	B	B	B	B	B									
	1.5pF (1R5)	B	B	B	B	B									
	1.8pF (1R8)	B	B	B	B	B									
	2.2pF (2R2)	B	B	B	B	B									
	2.7pF (2R7)	B	B	B	B	B									
	3.3pF (3R3)	B	B	B	B	B									
	3.9pF (3R9)	B	B	B	B	B									
	4.7pF (4R7)	B	B	B	B	B									
	5.6pF (5R6)	B	B	B	B	B									
	6.8pF (6R8)	B	B	B	B	B									
	8.2pF (8R2)	B	B	B	B	B									
	10pF (100)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	12pF (120)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	15pF (150)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	18pF (180)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	22pF (220)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	27pF (270)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	33pF (330)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	39pF (390)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	47pF (470)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	56pF (560)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	68pF (680)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	82pF (820)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	100pF (101)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	120pF (121)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	150pF (151)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	180pF (181)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	220pF (221)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	270pF (271)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	330pF (331)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	390pF (391)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	470pF (471)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	560pF (561)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	680pF (681)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	820pF (821)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	1,000pF (102)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	1,200pF (122)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	1,500pF (152)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	1,800pF (182)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	2,200pF (222)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	2,700pF (272)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	3,300pF (332)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	3,900pF (392)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	4,700pF (472)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	5,600pF (562)	B	B	B	B	B	C	C	C	C	C	D	D	D	
	6,800pF (682)	C	C	C	C	C	C	C	C	C	C	D	D	D	
	8,200pF (822)	D	D	D	D	D	C	C	C	C	C	D	D	D	
	0.010μF (103)	D	D	D	D	D	C	C	C	C	C	D	D	D	
	0.012μF (123)	P	P	P	P	P	D	D	D	D	D	D	D	D	
	0.015μF (153)	P	P	P	P	P	D	D	D	D	D	D	D	D	
	0.018μF (183)	P	P	P	P	P	K	K	K	K	K	D	D	D	
	0.022μF (223)	P	P	P	P	P	K	K	K	K	K	D	D	D	
	0.027μF (273)	P	P	P	P		K	K	K	K	K	D	D	D	
	0.033μF (333)	P	P	P	P		K	K	K	K	K	D	D	D	
	0.039μF (393)	P	P	P	P		K	K	K	K	K	M	M	M	
	0.047μF (473)	J*	J*	J*	J*		K	K	K	K	K	M	M	M	
	0.056μF (563)	J*	J*	J*	J*							M	M	M	
	0.068μF (683)	G*	G*	G*	G*							M	M	M	
	0.082μF (823)	G*	G*	G*	G*							M	M	M	
	0.1μF (104)	G*	G*	G*	G*							M	M	M	

1. The letter in cell is expressed the symbol of product thickness.

2. The letter in cell with “*” mark is expressed capacitance tolerance “J” ($\pm 5\%$) only.

3. For more information about products with special capacitance or other data, please contact WTC local representative.

Multilayer Ceramic Capacitors

7-3. Y5V Dielectric 0402, 0603, 0805 Sizes

DIELECTRIC		Y5V														
SIZE		0402					0603					0805				
RATED VOLTAGE (VDC)	6.3	10	16	25	50	6.3	10	16	25	50	6.3	10	16	25	50	100
Capacitance	0.010μF (103)	N	N	N	N		S	S	S	S		A	A	A	A	B
	0.015μF (153)	N	N	N	N		S	S	S	S		A	A	A	A	B
	0.022μF (223)	N	N	N	N		S	S	S	S		A	A	A	A	B
	0.033μF (333)	N	N	N	N		S	S	S	S		A	A	A	A	B
	0.047μF (473)	N	N	N			S	S	S	S		A	A	A	A	B
	0.068μF (683)	N	N	N			S	S	S	S		A	A	A	A	B
	0.10μF (104)	N	N	N			S	S	S	S		A	A	A	A	B
	0.15μF (154)	N	N				S	S	S	S		A	A	A	A	A
	0.22μF (224)	N	N	N			S	S	S	S		A	A	A	A	A
	0.33μF (334)	N	N	N			S	S	S	X		B	B	B	B	B
	0.47μF (474)	N	N	N			S	S	X	X		B	B	B	B	B
	0.68μF (684)	N					S	X	X			B	B	D	D	D
	1.0μF (105)	N/E	N/E				S	X	X			B	B	D	D	D
	1.5μF (155)						S					D	D			
	2.2μF (225)						S	S	X			D	D	I		
	3.3μF (335)											D	D			
	4.7μF (475)						X	X				D	D	I		
	6.8μF (685)											I				
	10μF (106)										I	I	I			
	22μF (226)										I	I				

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7-3. Y5V Dielectric 1206, 1210, 1812 Sizes

DIELECTRIC		Y5V																	
SIZE		1206						1210						1812					
RATED VOLTAGE (VDC)	6.3	10	16	25	50	100	6.3	10	16	25	35	50	100	10	16	25	50	100	
Capacitance	0.010μF (103)	B	B	B	B	B						C					D		
	0.015μF (153)	B	B	B	B	B						C					D		
	0.022μF (223)	B	B	B	B	B						C					D		
	0.033μF (333)	B	B	B	B	B						C					D		
	0.047μF (473)	B	B	B	B	B						C					D		
	0.068μF (683)	B	B	B	B	B						C					D		
	0.10μF (104)	B	B	B	B	B	C	C	C	C	C	C	D	D	D	D	D		
	0.15μF (154)	B	B	B	B	C	C	C	C	C	C	C	D	D	D	D	D		
	0.22μF (224)	B	B	B	B	C	C	C	C	C	C	C	D	D	D	D	D		
	0.33μF (334)	B	B	B	B	C	C	C	C	C	D	D	D	D	D	D	D		
	0.47μF (474)	B	B	B	B	C	C	C	C	C	D	D	D	D	D	D	D		
	0.68μF (684)	B	B	B	B	C	C	C	C	C	D	D	D	D	D	D	D		
	1.0μF (105)	C	C	C	C	C	C	C	C	C	D	D	D	D	D	D	D		
	1.5μF (155)	C	C	C	C	C	C	C	C	C	D	D	D	D	D	D	D		
	2.2μF (225)	C	C	C	J		C	C	C	G	D	D	D	D	D	D	D		
	3.3μF (335)	J	J	J			C	C	C		D	D	D	D	D	D	D		
	4.7μF (475)	J	J	J	P		C	C	D		G	D	D	D	D	D	D		
	6.8μF (685)	J	J				C	C	D	K	K	D	D	D	D	D	D		
	10μF (106)	J	J	P			D	D	G	K	K	D	D	D	K				
	22μF (226)	P	P				K	K				M							
	47μF (476)	P					K	K					M						
	100μF (107)						M												

1. The letter in cell is expressed the symbol of product thickness.

2. For more information about products with special capacitance or other data, please contact WTC local representative.

Multilayer Ceramic Capacitors

7-5. X6S Dielectric 0201, 0402, 0603, 0805, 1206, 1210 Sizes

Dielectric		X6S																							
Size		0201				0402				0603				0805				1206				1210			
Rated Voltage (VDC)		6.3	10	16	25	6.3	10	16	25	4	6.3	10	16	25	4	6.3	10	16	25	50	6.3	10	16	25	50
Capacitance	0.10μF (104)	L	L	L	L																				
	0.15μF (154)																								
	0.22μF (224)	L	L*																						
	0.33μF (334)																								
	0.47μF (474)					E																			
	0.68μF (684)																								
	1.0μF (105)	L*				E	E	E	E																
	1.5μF (155)																								
	2.2μF (225)		E	E	E					X	X														
	3.3μF (335)									X	X	X	X				I	I	I	I	I				
	4.7μF (475)																								
	6.8uF (685)																								
	10μF (106)		E*			X*	X*	X*		I	I	I	I	I				P							
	22μF (226)					X*	X*			I*	I*	I*	I*				P	P*	P				M		
	47μF (476)									I*	I*						P					M	M	M	
	100μF (107)																					M*	M*		

1. The letter in cell is expressed the symbol of product thickness.

2. The letter in cell with “ * ” mark is expressed product not in 10% (code “K”) tolerance.

7-6. X7S Dielectric 0402, 0603, 0805, 1206, 1210 Sizes

Dielectric		X7S																						
Size		0402				0603				0805				1206				1210						
Rated Voltage (VDC)		6.3	10	16	25	6.3	10	16	25	10	16	25	50	100	6.3	10	16	25	50	6.3	10	16	25	50
Capacitance	1.0μF (105)		E																					
	1.5μF (155)																							
	2.2μF (225)	E	E					X	X															
	3.3μF (335)							X	X															
	4.7μF (475)														I									
	6.8uF (685)														I	I								
	10μF (106)														I	I								
	22μF (226)																P*							
	47μF (476)																P*							
	100μF (107)																				M*			

1. The letter in cell is expressed the symbol of product thickness.

2. The letter in cell with “ * ” mark is expressed product not in 10% (code “K”) tolerance.

Multilayer Ceramic Capacitors

8. PACKAGING STYLE AND QUANTITY

Size	Thickness (mm)/Symbol	Paper tape		Plastic tape	
		7" reel	13" reel	7" reel	13" reel
0201 (0603)	0.30±0.03	L	15,000	70,000	-
	0.30±0.05	L	15,000	-	-
	0.30±0.09	L	15,000	-	-
0402 (1005)	0.50±0.05	N	10,000	50,000	-
	0.50+0.02/-0.05	Q	10,000	50,000	-
	0.50±0.20	E	10,000	-	-
0603 (1608)	0.50±0.10	H	4,000	-	-
	0.80±0.07	S	4,000	15,000	-
	0.80+0.15/-0.10	X	4,000	15,000	-
0805 (2012)	0.50±0.10	H	4,000	15,000	-
	0.60±0.10	A	4,000	15,000	-
	0.80±0.10	B	4,000	15,000	-
	0.85±0.10	T	4,000	15,000	-
	1.25±0.10	D	-	-	3,000
	1.25±0.20	I	-	-	3,000
1206 (3216)	0.80±0.10	B	4,000	15,000	-
	0.85±0.10	T	4,000	15,000	-
	0.95±0.10	C	-	-	3,000
	1.15±0.15	J	-	-	3,000
	1.25±0.10	D	-	-	3,000
	1.60±0.20	G	-	-	2,000
	1.60+0.30/-0.10	P	-	-	2,000
1210 (3225)	0.85±0.10	T	-	-	3,000
	0.95±0.10	C	-	-	3,000
	1.25±0.10	D	-	-	3,000
	1.60±0.20	G	-	-	2,000
	2.00±0.20	K	-	-	1,000
	2.50±0.30	M	-	-	1,000
1808 (4520)	1.25±0.10	D	-	-	2,000
	1.10±0.15	F	-	-	2,000
	1.60±0.20	G	-	-	2,000
	2.00±0.20	K	-	-	1,000
	1.25±0.10	D	-	-	2,000
1812 (4532)	1.60±0.20	G	-	-	1,000
	2.00±0.20	K	-	-	1,000
	2.50±0.30	M	-	-	500
	2.80±0.30	U	-	-	500
	1.25±0.10	D	-	-	1,000

Unit: pieces

Multilayer Ceramic Capacitors

9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements																																																																																																																															
1.	Visual and Mechanical	--	<ul style="list-style-type: none"> * No remarkable defect. * Dimensions to conform to individual specification sheet. 																																																																																																																															
2.	Capacitance	Class I: (NPO) $\leq 1000\text{pF}, 1.0\pm 0.2\text{Vrms} \cdot 1\text{MHz}\pm 10\%$ $> 1000\text{pF}, 1.0\pm 0.2\text{Vrms} \cdot 1\text{KHz}\pm 10\%$	<ul style="list-style-type: none"> * Shall not exceed the limits given in the detailed spec. 																																																																																																																															
3.	Q/D.F. (Dissipation Factor)	Class II: (X7R, X7E, X6S, X5R, X7S, Y5V) $C \leq 10\mu\text{F}, 1.0\pm 0.2\text{Vrms} \cdot 1\text{KHz}\pm 10\% **$ $C > 10\mu\text{F}, 0.5\pm 0.2\text{Vrms} \cdot 120\text{Hz}\pm 20\%$ ** Test condition: $0.5\pm 0.2\text{Vrms} \cdot 1\text{KHz}\pm 10\%$ X7R: 0805=106(6.3V), 0603/475(6.3V) X5R: 0201 \geq 224 (6.3V, 10V, 16V) ^{#1} , 0402 \geq 475 (6.3V, 16V), 0402 \geq 225(10V), 0603=106 (6.3V, 10V), TT18X \geq 475(10V) , TT15X series X6S: 0201 \geq 104 (6.3V, 10V) ^{#1} , 0402 \geq 225 (6.3V), 0402/475 (10V), 0603/106 (6.3V), X7S: 0402/225(6.3V) #1 Excluding X5R/0201/105(6.3V); 225(10V), X6S/0201/104(10V) ($1.0\pm 0.2\text{Vrms} \cdot 1\text{KHz}\pm 10\%$)	<p>NPO: Cap\geq30pF, Q\geq1000; Cap<30pF, Q\geq400+20C X7R, X5R, X6S, X7S:</p> <table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F. \leq</th> <th>Exception of D.F. \leq</th> </tr> </thead> <tbody> <tr> <td rowspan="3">50V</td> <td>$\leq 3\%$</td> <td>1206\geq0.47μF</td> </tr> <tr> <td>$\leq 5\%$</td> <td>0805$> 0.1\mu\text{F}$; 0603$\geq 0.068\mu\text{F}$; 1206$> 1\mu\text{F}$; 1210$\geq 2.2\mu\text{F}$; TT series</td> </tr> <tr> <td>$\leq 10\%$</td> <td>0805$> 0.22\mu\text{F}$; 1210$\geq 3.3\mu\text{F}$</td> </tr> <tr> <td rowspan="3">35V</td> <td>$\leq 3\%$</td> <td>0201(50V); 0603$\geq 0.047\mu\text{F}$; 0805$\geq 0.18\mu\text{F}$; 1206$\geq 0.47\mu\text{F}$</td> </tr> <tr> <td>$\leq 5\%$</td> <td>0201$\geq 0.01\mu\text{F}$; 1210$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td>$\leq 10\%$</td> <td>0402$\geq 0.012\mu\text{F}$; 0603$> 0.1\mu\text{F}$; 0805$\geq 1\mu\text{F}$; 1206$\geq 2.2\mu\text{F}$; 1210$\geq 10\mu\text{F}$; TT series</td> </tr> <tr> <td rowspan="3">25V</td> <td>$\leq 3.5\%$</td> <td>0603$\geq 1\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 2.2\mu\text{F}$; 1210$\geq 10\mu\text{F}$</td> </tr> <tr> <td>$\leq 5\%$</td> <td>0201$\geq 0.01\mu\text{F}$; 0805$\geq 1\mu\text{F}$; 1210$\geq 10\mu\text{F}$</td> </tr> <tr> <td>$\leq 7\%$</td> <td>0603$\geq 0.33\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td rowspan="3">16V</td> <td>$\leq 3.5\%$</td> <td>0201$\geq 0.1\mu\text{F}$; 0402$\geq 0.10\mu\text{F}$&(0402/X7R$\geq 0.056\mu\text{F}$); TT series 0603$\geq 0.47\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 6.8\mu\text{F}$; 1210$\geq 22\mu\text{F}$</td> </tr> <tr> <td>$\leq 5\%$</td> <td>0402$\geq 0.47\mu\text{F}$</td> </tr> <tr> <td>$\leq 10\%$</td> <td>0201$\geq 0.1\mu\text{F}$; 0402$\geq 0.033\mu\text{F}$; 0603$\geq 0.15\mu\text{F}$;</td> </tr> <tr> <td rowspan="3">10V</td> <td>$\leq 5\%$</td> <td>0805$\geq 0.68\mu\text{F}$; 1206$\geq 2.2\mu\text{F}$; 1210$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td>$\leq 10\%$</td> <td>0201$\geq 0.1\mu\text{F}$; 0402$\geq 0.33\mu\text{F}$(0402/X7R$\geq 0.22\mu\text{F}$); TT series 0603$\geq 0.33\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 2.2\mu\text{F}$; 1210$\geq 22\mu\text{F}$; TT series</td> </tr> <tr> <td>$\leq 15\%$</td> <td>0201$\geq 0.1\mu\text{F}$; 0402$\geq 1\mu\text{F}$</td> </tr> <tr> <td rowspan="2">6.3V</td> <td>$\leq 10\%$</td> <td>0201$\geq 0.1\mu\text{F}$; 0402$\geq 1\mu\text{F}$; 0603$\geq 10\mu\text{F}$; 0805$\geq 4.7\mu\text{F}$; 1206$\geq 47\mu\text{F}$; 1210$\geq 100\mu\text{F}$; TT series</td> </tr> <tr> <td>$\leq 20\%$</td> <td>0402$\geq 2.2\mu\text{F}$</td> </tr> <tr> <td>4V</td> <td>$\leq 15\%$</td> <td>---</td> </tr> <tr> <td colspan="6">Y5V:</td></tr> <tr> <td colspan="5"> <table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F. \leq</th> <th>Exception of D.F. \leq</th> </tr> </thead> <tbody> <tr> <td rowspan="2">50V</td> <td>$\leq 5\%$</td> <td>0603$\geq 0.1\mu\text{F}$; 0805$\geq 0.47\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; TT series</td> </tr> <tr> <td>$\leq 12.5\%$</td> <td>1210$\geq 6.8\mu\text{F}$</td> </tr> <tr> <td rowspan="3">35V</td> <td>$\leq 7\%$</td> <td>---</td> </tr> <tr> <td>$\leq 7\%$</td> <td>0402$\geq 0.047\mu\text{F}$; 0603$\geq 0.1\mu\text{F}$; 0805$\geq 0.33\mu\text{F}$; 1206$\geq 1\mu\text{F}$; 1210$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td>$\leq 9\%$</td> <td>0402$\geq 0.068\mu\text{F}$; 0603$\geq 0.47\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; 1210$\geq 22\mu\text{F}$; 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D.F. \leq	Exception of D.F. \leq	50V	$\leq 3\%$	1206 \geq 0.47 μF	$\leq 5\%$	0805 $> 0.1\mu\text{F}$; 0603 $\geq 0.068\mu\text{F}$; 1206 $> 1\mu\text{F}$; 1210 $\geq 2.2\mu\text{F}$; TT series	$\leq 10\%$	0805 $> 0.22\mu\text{F}$; 1210 $\geq 3.3\mu\text{F}$	35V	$\leq 3\%$	0201(50V); 0603 $\geq 0.047\mu\text{F}$; 0805 $\geq 0.18\mu\text{F}$; 1206 $\geq 0.47\mu\text{F}$	$\leq 5\%$	0201 $\geq 0.01\mu\text{F}$; 1210 $\geq 4.7\mu\text{F}$	$\leq 10\%$	0402 $\geq 0.012\mu\text{F}$; 0603 $> 0.1\mu\text{F}$; 0805 $\geq 1\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 10\mu\text{F}$; TT series	25V	$\leq 3.5\%$	0603 $\geq 1\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 10\mu\text{F}$	$\leq 5\%$	0201 $\geq 0.01\mu\text{F}$; 0805 $\geq 1\mu\text{F}$; 1210 $\geq 10\mu\text{F}$	$\leq 7\%$	0603 $\geq 0.33\mu\text{F}$; 1206 $\geq 4.7\mu\text{F}$	16V	$\leq 3.5\%$	0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 0.10\mu\text{F}$ &(0402/X7R $\geq 0.056\mu\text{F}$); TT series 0603 $\geq 0.47\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 6.8\mu\text{F}$; 1210 $\geq 22\mu\text{F}$	$\leq 5\%$	0402 $\geq 0.47\mu\text{F}$	$\leq 10\%$	0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 0.033\mu\text{F}$; 0603 $\geq 0.15\mu\text{F}$;	10V	$\leq 5\%$	0805 $\geq 0.68\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 4.7\mu\text{F}$	$\leq 10\%$	0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 0.33\mu\text{F}$ (0402/X7R $\geq 0.22\mu\text{F}$); TT series 0603 $\geq 0.33\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 22\mu\text{F}$; TT series	$\leq 15\%$	0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 1\mu\text{F}$	6.3V	$\leq 10\%$	0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 1\mu\text{F}$; 0603 $\geq 10\mu\text{F}$; 0805 $\geq 4.7\mu\text{F}$; 1206 $\geq 47\mu\text{F}$; 1210 $\geq 100\mu\text{F}$; TT series	$\leq 20\%$	0402 $\geq 2.2\mu\text{F}$	4V	$\leq 15\%$	---	Y5V:						<table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F. \leq</th> <th>Exception of D.F. \leq</th> </tr> </thead> <tbody> <tr> <td rowspan="2">50V</td> <td>$\leq 5\%$</td> <td>0603$\geq 0.1\mu\text{F}$; 0805$\geq 0.47\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; TT series</td> </tr> <tr> <td>$\leq 12.5\%$</td> <td>1210$\geq 6.8\mu\text{F}$</td> </tr> <tr> <td rowspan="3">35V</td> <td>$\leq 7\%$</td> <td>---</td> </tr> <tr> <td>$\leq 7\%$</td> <td>0402$\geq 0.047\mu\text{F}$; 0603$\geq 0.1\mu\text{F}$; 0805$\geq 0.33\mu\text{F}$; 1206$\geq 1\mu\text{F}$; 1210$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td>$\leq 9\%$</td> <td>0402$\geq 0.068\mu\text{F}$; 0603$\geq 0.47\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; 1210$\geq 22\mu\text{F}$; TT series</td> </tr> <tr> <td rowspan="2">25V</td> <td>$\leq 7\%$</td> <td>0402$\geq 0.068\mu\text{F}$; 0603$\geq 0.68\mu\text{F}$</td> </tr> <tr> <td>$\leq 9\%$</td> <td>0402$\geq 0.22\mu\text{F}$</td> </tr> <tr> <td rowspan="2">16V</td> <td>$\leq 9\%$</td> <td>0603$\geq 2.2\mu\text{F}$; 0805$\geq 3.3\mu\text{F}$; 1206$\geq 10\mu\text{F}$;</td> </tr> <tr> <td>$\leq 12.5\%$</td> <td>1210$\geq 22\mu\text{F}$; 1812$\geq 47\mu\text{F}$; TT series</td> </tr> <tr> <td rowspan="2">10V</td> <td>$\leq 12.5\%$</td> <td>0402$\geq 0.47\mu\text{F}$</td> </tr> <tr> <td>$\leq 20\%$</td> <td>---</td> </tr> </tbody> </table>					Rated vol.	D.F. \leq	Exception of D.F. \leq	50V	$\leq 5\%$	0603 $\geq 0.1\mu\text{F}$; 0805 $\geq 0.47\mu\text{F}$; 1206 $\geq 4.7\mu\text{F}$; TT series	$\leq 12.5\%$	1210 $\geq 6.8\mu\text{F}$	35V	$\leq 7\%$	---	$\leq 7\%$	0402 $\geq 0.047\mu\text{F}$; 0603 $\geq 0.1\mu\text{F}$; 0805 $\geq 0.33\mu\text{F}$; 1206 $\geq 1\mu\text{F}$; 1210 $\geq 4.7\mu\text{F}$	$\leq 9\%$	0402 $\geq 0.068\mu\text{F}$; 0603 $\geq 0.47\mu\text{F}$; 1206 $\geq 4.7\mu\text{F}$; 1210 $\geq 22\mu\text{F}$; TT series	25V	$\leq 7\%$	0402 $\geq 0.068\mu\text{F}$; 0603 $\geq 0.68\mu\text{F}$	$\leq 9\%$	0402 $\geq 0.22\mu\text{F}$	16V	$\leq 9\%$	0603 $\geq 2.2\mu\text{F}$; 0805 $\geq 3.3\mu\text{F}$; 1206 $\geq 10\mu\text{F}$;	$\leq 12.5\%$	1210 $\geq 22\mu\text{F}$; 1812 $\geq 47\mu\text{F}$; TT series	10V	$\leq 12.5\%$	0402 $\geq 0.47\mu\text{F}$	$\leq 20\%$	---	4.	Dielectric Strength	* To apply voltage ($\leq 100\text{V}$) 250%. * Duration: 1 to 5 sec. * Charge and discharge current less than 50mA.	<ul style="list-style-type: none"> * No evidence of damage or flash over during test. 			5.	Insulation Resistance	To apply rated voltage for MAX. 120sec. *Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.	<p>10GΩ or $RxC \geq 500\Omega\text{-F}$ whichever is smaller.</p> <p>Class II (X7R, X7E, X5R, X6S, X7S, Y5V):</p> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>Insulation Resistance</th> </tr> </thead> <tbody> <tr> <td>100V: All X7R</td> <td rowspan="6">10GΩ or $RxC \geq 100\Omega\text{-F}$ whichever is smaller.</td> </tr> <tr> <td>50V: 0402$> 0.01\mu\text{F}$; 0603$\geq 1\mu\text{F}$; 0805$\geq 1\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; 1210$\geq 4.7\mu\text{F}$</td> </tr> <tr> <td>35V: 0805$\geq 2.2\mu\text{F}$; 1206$\geq 2.2\mu\text{F}$; 1210$\geq 10\mu\text{F}$</td> </tr> <tr> <td>25V: 0402$\geq 1\mu\text{F}$; 0603$\geq 2.2\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 10\mu\text{F}$; 1210$\geq 10\mu\text{F}$</td> </tr> <tr> <td>16V: 0201$\geq 0.1\mu\text{F}$; 0402$\geq 0.22\mu\text{F}$; 0603$\geq 1\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 10\mu\text{F}$; 1210$\geq 47\mu\text{F}$</td> </tr> <tr> <td>10V: 0201$\geq 47\mu\text{F}$; 0402$\geq 0.47\mu\text{F}$; 0603$\geq 0.47\mu\text{F}$; 0805$\geq 2.2\mu\text{F}$; 1206$\geq 4.7\mu\text{F}$; 1210$\geq 47\mu\text{F}$</td> </tr> <tr> <td>6.3V ; 4V ; TT series; Size\geq1812</td> <td></td> </tr> </tbody> </table> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>Insulation Resistance</th> </tr> </thead> <tbody> <tr> <td>All X6S items, All X7S items</td> <td rowspan="6">$RxC \geq 50\Omega\text{-F}$</td> </tr> <tr> <td>100V: 1210$\geq 3.3\mu\text{F}$</td> </tr> <tr> <td>50V: 0402$\geq 0.1\mu\text{F}$; 0603$\geq 2.2\mu\text{F}$; 0805$\geq 10\mu\text{F}$; 1206$\geq 10\mu\text{F}$</td> </tr> <tr> <td>35V: 0603$\geq 1\mu\text{F}$</td> </tr> <tr> <td>25V: 0201$\geq 0.1\mu\text{F}$; 0402$\geq 2.2\mu\text{F}$; 0603$\geq 10\mu\text{F}$; 0805$\geq 10\mu\text{F}$; 1206$\geq 22\mu\text{F}$</td> </tr> <tr> <td>16V: 0603$\geq 10\mu\text{F}$; 0402$\geq 1\mu\text{F}$; 0201$\geq 0.22\mu\text{F}$</td> </tr> <tr> <td>10V: 0201$\geq 0.1\mu\text{F}$; 0402$\geq 1\mu\text{F}$; 0603$\geq 10\mu\text{F}$; 0805$\geq 47\mu\text{F}$; TT21$\geq 4.7\mu\text{F}$</td> <td></td> </tr> <tr> <td>6.3V: 0201$\geq 0.1\mu\text{F}$; 0603$\geq 4.7\mu\text{F}$; 0805$\geq 47\mu\text{F}$; 1206$\geq 10\mu\text{F}$; TT15$\geq 1.0\mu\text{F}$</td> <td></td> </tr> <tr> <td>4V: 0603$\geq 22\mu\text{F}$; 0805$\geq 47\mu\text{F}$; 1206$\geq 100\mu\text{F}$</td> <td></td> </tr> </tbody> </table>					Rated voltage	Insulation Resistance	100V: All X7R	10GΩ or $RxC \geq 100\Omega\text{-F}$ whichever is smaller.	50V: 0402 $> 0.01\mu\text{F}$; 0603 $\geq 1\mu\text{F}$; 0805 $\geq 1\mu\text{F}$; 1206 $\geq 4.7\mu\text{F}$; 1210 $\geq 4.7\mu\text{F}$	35V: 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 10\mu\text{F}$	25V: 0402 $\geq 1\mu\text{F}$; 0603 $\geq 2.2\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 10\mu\text{F}$; 1210 $\geq 10\mu\text{F}$	16V: 0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 0.22\mu\text{F}$; 0603 $\geq 1\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 10\mu\text{F}$; 1210 $\geq 47\mu\text{F}$	10V: 0201 $\geq 47\mu\text{F}$; 0402 $\geq 0.47\mu\text{F}$; 0603 $\geq 0.47\mu\text{F}$; 0805 $\geq 2.2\mu\text{F}$; 1206 $\geq 4.7\mu\text{F}$; 1210 $\geq 47\mu\text{F}$	6.3V ; 4V ; TT series; Size \geq 1812		Rated voltage	Insulation Resistance	All X6S items, All X7S items	$RxC \geq 50\Omega\text{-F}$	100V: 1210 $\geq 3.3\mu\text{F}$	50V: 0402 $\geq 0.1\mu\text{F}$; 0603 $\geq 2.2\mu\text{F}$; 0805 $\geq 10\mu\text{F}$; 1206 $\geq 10\mu\text{F}$	35V: 0603 $\geq 1\mu\text{F}$	25V: 0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 2.2\mu\text{F}$; 0603 $\geq 10\mu\text{F}$; 0805 $\geq 10\mu\text{F}$; 1206 $\geq 22\mu\text{F}$	16V: 0603 $\geq 10\mu\text{F}$; 0402 $\geq 1\mu\text{F}$; 0201 $\geq 0.22\mu\text{F}$	10V: 0201 $\geq 0.1\mu\text{F}$; 0402 $\geq 1\mu\text{F}$; 0603 $\geq 10\mu\text{F}$; 0805 $\geq 47\mu\text{F}$; TT21 $\geq 4.7\mu\text{F}$		6.3V: 0201 $\geq 0.1\mu\text{F}$; 0603 $\geq 4.7\mu\text{F}$; 0805 $\geq 47\mu\text{F}$; 1206 $\geq 10\mu\text{F}$; TT15 $\geq 1.0\mu\text{F}$		4V: 0603 $\geq 22\mu\text{F}$; 0805 $\geq 47\mu\text{F}$; 1206 $\geq 100\mu\text{F}$	
Rated vol.	D.F. \leq	Exception of D.F. \leq																																																																																																																																
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	$\leq 10\%$	0402 $\geq 0.012\mu\text{F}$; 0603 $> 0.1\mu\text{F}$; 0805 $\geq 1\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 10\mu\text{F}$; TT series																																																																																																																																
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10V	$\leq 5\%$	0805 $\geq 0.68\mu\text{F}$; 1206 $\geq 2.2\mu\text{F}$; 1210 $\geq 4.7\mu\text{F}$																																																																																																																																
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Multilayer Ceramic Capacitors

No.	Item	Test Condition	Requirements																																																						
6.	Temperature Coefficient	<p>With no electrical load.</p> <table border="1"> <tr><td>T.C.</td><td>Operating Temp</td></tr> <tr><td>NPO</td><td>-55~125°C at 25°C</td></tr> <tr><td>X7R</td><td>-55~125°C at 25°C</td></tr> <tr><td>X7S</td><td>-55 ~ 125°C at 25°C</td></tr> <tr><td>X5R</td><td>-55~ 85°C at 25°C</td></tr> <tr><td>X6S</td><td>-55~105°C at 25°C</td></tr> <tr><td>Y5V</td><td>-25~ 85°C at 20°C</td></tr> </table> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24± 2 hrs at room temp. * Measurement voltage for Class II:</p> <table border="1"> <tr><td>01005</td><td>0201</td></tr> <tr><td>Cap≤0.01μF: 0.5V</td><td>Cap<0.1μF: 1V</td></tr> <tr><td>Cap>0.01μF: 0.2V</td><td>0.1μF≤Cap<1μF: 0.2V</td></tr> <tr><td></td><td>Cap≥1μF: 0.1V</td></tr> <tr><td>0402</td><td>0603</td></tr> <tr><td>Cap<1μF: 1V</td><td>Cap≥1μF: 1V</td></tr> <tr><td>Cap=1μF: 0.5V</td><td>1μF<Cap≤4.7μF: 0.5V</td></tr> <tr><td>1μF<Cap<10μF: 0.2V</td><td>Cap>4.7μF: 0.2V</td></tr> <tr><td>Cap≥10μF: 0.1V</td><td></td></tr> <tr><td>0805</td><td>1206/1210</td></tr> <tr><td>Cap<10μF: 1V</td><td>Cap≤10μF: 1V</td></tr> <tr><td>Cap=10μF: 0.5V</td><td>10μF<Cap≤100μF: 0.5V</td></tr> <tr><td>Cap>10μF: 0.2V</td><td>Cap>100μF: 0.2V</td></tr> </table>	T.C.	Operating Temp	NPO	-55~125°C at 25°C	X7R	-55~125°C at 25°C	X7S	-55 ~ 125°C at 25°C	X5R	-55~ 85°C at 25°C	X6S	-55~105°C at 25°C	Y5V	-25~ 85°C at 20°C	01005	0201	Cap≤0.01μF: 0.5V	Cap<0.1μF: 1V	Cap>0.01μF: 0.2V	0.1μF≤Cap<1μF: 0.2V		Cap≥1μF: 0.1V	0402	0603	Cap<1μF: 1V	Cap≥1μF: 1V	Cap=1μF: 0.5V	1μF<Cap≤4.7μF: 0.5V	1μF<Cap<10μF: 0.2V	Cap>4.7μF: 0.2V	Cap≥10μF: 0.1V		0805	1206/1210	Cap<10μF: 1V	Cap≤10μF: 1V	Cap=10μF: 0.5V	10μF<Cap≤100μF: 0.5V	Cap>10μF: 0.2V	Cap>100μF: 0.2V	<table border="1"> <tr><td>T.C.</td><td>Capacitance Change</td></tr> <tr><td>NPO</td><td>Within ±30ppm/C</td></tr> <tr><td>X7R</td><td>Within ±15%</td></tr> <tr><td>X7S</td><td>Within ±22%</td></tr> <tr><td>X5R</td><td>Within ±15%</td></tr> <tr><td>X6S</td><td>Within ±22%</td></tr> <tr><td>Y5V</td><td>Within +30%/-80%</td></tr> </table>	T.C.	Capacitance Change	NPO	Within ±30ppm/C	X7R	Within ±15%	X7S	Within ±22%	X5R	Within ±15%	X6S	Within ±22%	Y5V	Within +30%/-80%
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7.	Adhesive Strength of Termination	<p>* Pressurizing force : 2N (0201) and 5N (≤0603) and 10N (>0603)</p> <p>* Test time: 10±1 sec.</p>	* No remarkable damage or removal of the terminations.																																																						
8.	Vibration Resistance	<p>* Vibration frequency: 10~55 Hz/min.</p> <p>* Total amplitude: 1.5mm</p> <p>* Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)</p> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24± 2 hrs at room temp. *Cap./DF(Q) Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</p>	<p>* No remarkable damage.</p> <p>* Cap change and Q/D.F.: To meet initial spec.</p>																																																						
9.	Solderability	<p>* Solder temperature: 235±5°C</p> <p>* Dipping time: 2±0.5 sec.</p>	95% min. coverage of all metallized area.																																																						
10.	Bending Test	<p>* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until the deflection becomes 1 mm and then the pressure shall be maintained for 5±1 sec.</p> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24± 2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs.</p>	<p>* No remarkable damage.</p> <p>* Cap change : NPO: within ±5% or 0.5pF whichever is larger X7R, X5R, X6S, X7S: within ±12.5% Y5V: within ±30% (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)</p>																																																						
11.	Resistance to Soldering Heat	<p>* Solder temperature: 260±5°C</p> <p>* Dipping time: 10±1 sec</p> <p>* Preheating: 120 to 150°C for 1 minute before immerse the capacitor in a eutectic solder.</p> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24± 2 hrs at room temp. *Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</p>	<p>* No remarkable damage.</p> <p>* Cap change: NPO: within ±2.5% or 0.25pF whichever is larger X7R, X5R, X6S, X7S: within ±7.5% Y5V: within ±20%</p> <p>* Q/D.F., I.R. and dielectric strength: To meet initial requirements.</p> <p>* 25% max. leaching on each edge.</p>																																																						
12.	Temperature Cycle	<p>* Conduct the five cycles according to the temperatures and time.</p> <table border="1"> <tr><td>Step</td><td>Temp. (°C)</td><td>Time (min.)</td></tr> <tr><td>1</td><td>Min. operating temp. +0/-3</td><td>30±3</td></tr> <tr><td>2</td><td>Room temp.</td><td>2~3</td></tr> <tr><td>3</td><td>Max. operating temp. +3/-0</td><td>30±3</td></tr> <tr><td>4</td><td>Room temp.</td><td>2~3</td></tr> </table> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24± 2 hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</p>	Step	Temp. (°C)	Time (min.)	1	Min. operating temp. +0/-3	30±3	2	Room temp.	2~3	3	Max. operating temp. +3/-0	30±3	4	Room temp.	2~3	<p>* No remarkable damage.</p> <p>* Cap change : NPO: within ±2.5% or 0.25pF whichever is larger X7R, X5R, X6S, X7S: within ±7.5% Y5V: within ±20%</p> <p>* Q/D.F., I.R. and dielectric strength: To meet initial requirements.</p>																																							
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Multilayer Ceramic Capacitors

No.	Item	Test Condition	Requirements																																																			
13.	Humidity (Damp Heat) Steady State	<ul style="list-style-type: none"> *Test temp.: 40±2°C *Humidity: 90~95%RH *Test time: 500+24/-hrs. *Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. 	<ul style="list-style-type: none"> No remarkable damage. Cap change: NPO: within ±5% or 0.5pF whichever is larger X7R, X5R, X6S, X7S: $\geq 10V^{**}$, within ±12.5%; $\leq 6.3V$ within ±25%; TT series & C ≥ 1uF, within ±25% $**10V: 0603 \geq 4.7\mu F; 0402 \geq 1\mu F; 0201 \geq 0.1\mu F$, within ±25%; $Y5V: \geq 10V$, within ±30%; $\leq 6.3V$, within +30/-40% Q/D.F. value: NPO: More than 30pF Q≥350, 10pF≤C≤30pF, Q≥275+2.5C Less than 10pF Q≥200+10C <p>X7R, X5R, X6S, X7S:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Rated Vol.</th> <th>D.F. ≤</th> <th>Exception of D.F. ≤</th> </tr> </thead> <tbody> <tr> <td rowspan="3">50V</td> <td>≤ 3%</td> <td>≤ 6% 1206 ≥ 0.47μF ≤ 7.5% 0805 ≥ 0.1μF, 0603 ≥ 0.068μF, 1206 > 1μF; 1210 ≥ 2.2μF; TT series ≤ 20% 0805 ≥ 0.22μF; 1210 ≥ 3.3μF</td> </tr> <tr> <td>≤ 3%</td> <td>≤ 6% 0201(50V); 0603 ≥ 0.047μF; 0805 ≥ 0.18μF; 1206 ≥ 0.47μF ≤ 10% 0201 ≥ 0.01μF; 1210 ≥ 4.7μF ≤ 20% 0402 ≥ 0.012μF; 0603 > 0.1μF; 0805 ≥ 1μF; 1206 ≥ 2.2μF; 1210 ≥ 10μF; TT series</td> </tr> <tr> <td>≤ 5%</td> <td>≤ 20% 0603 ≥ 1μF; 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*I.R.: $\geq 10V$, $1G\Omega$ or $50\Omega\text{-F}$ whichever is smaller.

Class II (X7R, X5R, X6S, X7S, Y5V)

Rated voltage	Insulation Resistance
100V: All X7R; 1210 ≥ 3.3μF	
50V: 0402 > 0.01μF; 0603 ≥ 1μF; 0805 ≥ 1μF; 1206 ≥ 4.7μF; 1210 ≥ 4.7μF	
35V: 0603 ≥ 1μF; 0805 ≥ 2.2μF; 1206 ≥ 2.2μF; 1210 ≥ 10μF	
25V: 0201 ≥ 0.1μF; 0402 ≥ 0.22μF; 0603 ≥ 2.2μF; 0805 ≥ 2.2μF; 1206 ≥ 10μF; 1210 ≥ 10μF	1GΩ or Rx $C \geq 10\Omega\text{-F}$ whichever is smaller.
16V: 0201 ≥ 0.1μF; 0402 ≥ 0.22μF; 0603 ≥ 1μF; 0805 ≥ 2.2μF; 1206 ≥ 10μF; 1210 ≥ 47μF	
10V: 0201 ≥ 47nF; 0402 ≥ 0.47μF; 0603 ≥ 0.47μF; 0805 ≥ 2.2μF; 1206 ≥ 4.7μF; 1210 ≥ 47μF	
6.3V ; 4V ; TT series ; All X6S/X7S items; Size ≥ 1812	

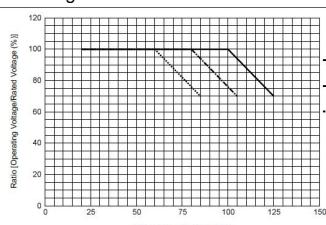
Multilayer Ceramic Capacitors

No	Item	Test Condition	Requirements																																								
14	Humidity (Damp Heat) Load	<ul style="list-style-type: none"> *Test temp. : 40±2°C *Humidity : 90~95%RH *Test time : 500+24/-0 hrs. *To apply voltage : <ul style="list-style-type: none"> Rated voltage (MAX. 500V) *Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. 	<ul style="list-style-type: none"> * No remarkable damage. Cap change: <ul style="list-style-type: none"> NPO: ±7.5% or 0.75pF whichever is larger. X7R, X5R, X6S, X7S: ≥10V**, within ±12.5%; ≤6.3V within ±25%; TT series & C≥1uF, within ±25% **10V: 0603≥4.7μF; 0402≥1μF; 0201≥0.1μF, within ±25%; Y5V: ≥10V, within ±30%; ≤6.3V, within +30~-40% Q.D.F. value: <ul style="list-style-type: none"> NPO: C≥30pF, Q≥200; C<30pF, Q≥100+10/3C X7R, X5R, X6S, X7S: <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Rated vol.</th> <th>D.F.≤</th> <th>Exception of D.F.≤</th> </tr> </thead> <tbody> <tr> <td rowspan="3">≥100V</td> <td>≤3%</td> <td> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td>≤6%</td> <td>1206≥0.47μF</td> </tr> <tr> <td>≤7.5%</td> <td>0805≥0.1μF, 0603≥0.068μF, 1206>1μF; 1210≥2.2μF; TT series</td> </tr> <tr> <td>≤20%</td> <td>0805≥0.22μF; 1210≥3.3μF</td> </tr> </table></td></tr></tbody> </table>	Rated vol.	D.F.≤	Exception of D.F.≤	≥100V	≤3%	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td>≤6%</td> <td>1206≥0.47μF</td> </tr> <tr> <td>≤7.5%</td> <td>0805≥0.1μF, 0603≥0.068μF, 1206>1μF; 1210≥2.2μF; TT series</td> </tr> <tr> <td>≤20%</td> <td>0805≥0.22μF; 1210≥3.3μF</td> </tr> </table>	≤6%	1206≥0.47μF	≤7.5%	0805≥0.1μF, 0603≥0.068μF, 1206>1μF; 1210≥2.2μF; TT series	≤20%	0805≥0.22μF; 1210≥3.3μF																												
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16V (C≥1.0μF)	≤12.5%	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td>≤20%</td> <td>0603≥2.2μF; 0805≥3.3μF; 1206≥10μF; 1210≥22μF; 1812≥47μF</td> </tr> <tr> <td>≤30%</td> <td>0402≥0.47μF</td> </tr> <tr> <td>≤30%</td> <td>---</td> </tr> </table>	≤20%	0603≥2.2μF; 0805≥3.3μF; 1206≥10μF; 1210≥22μF; 1812≥47μF	≤30%	0402≥0.47μF	≤30%	---																																			
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10V	≤20%	0402≥0.47μF																																									
	≤20%	---																																									
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6.3V	≤30%	---																																									

I.R.: ≥10V, 500MΩ or 25 Ω-F whichever is smaller.
Class II (X7R, X5R, X6S, X7S, Y5V)

Rated voltage	Insulation Resistance
100V: All X7R; 1210≥3.3μF	500MΩ or RxC≥5 Ω-F whichever is smaller.
50V: 0402>0.01μF; 0603≥1μF; 0805≥1μF; 1206≥4.7μF; 1210≥4.7μF	
35V: 0603≥1μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥10μF	
25V: 0201≥0.1μF; 0402≥0.22μF; 0603≥2.2μF; 0805≥2.2μF; 1206≥10μF; 1210≥10μF	
16V: 0201≥0.1μF; 0402≥0.22μF; 0603≥1μF; 0805≥2.2μF; 1206≥10μF; 1210≥47μF	
10V: 0201≥47nF; 0402≥0.47μF; 0603≥0.47μF; 0805≥2.2μF; 1206≥4.7μF; 1210≥47μF	
6.3V ; 4V ; TT series ; All X6S/X7S items; Size≥1812	

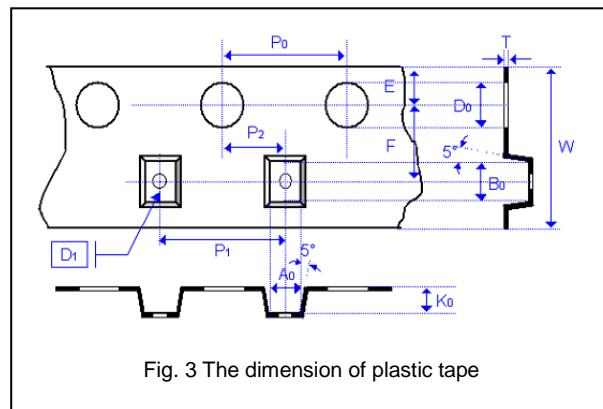
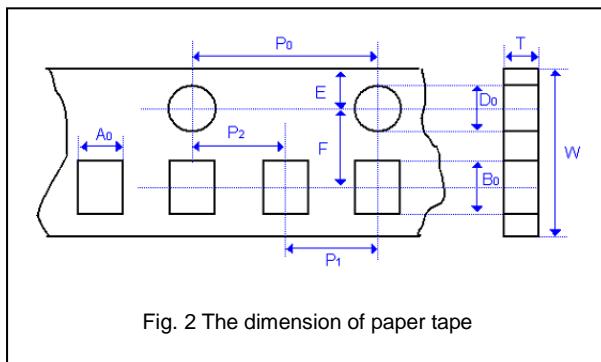
Multilayer Ceramic Capacitors

No	Item	Test Condition				Requirements																																																																																		
15.	High Temperature Load (Endurance)	Test temp. : NP0, X7R/X7E/X7S: 125±3°C X6S: 105±3°C X5R, Y5V: 85±3°C Test time: 1000+24/-0 hrs. To apply voltage: (1) $\leq 6.3V$ or $C \geq 10\mu F$ or TT series: 150% of rated voltage. (2) $10V \leq Ur < 500V$: 200% of rated voltage. (3) 500V: 150% of rated voltage. (4) $Ur \geq 630V$: 120% of rated voltage. (5) 100% of rated voltage for below range.				* No remarkable damage. Cap change: NP0: ±3.0% or ±0.3pF whichever is larger X7R, X5R, X6S, X7S: $\geq 10V^{**}$, within ±12.5%; $\leq 6.3V$ within ±25%; TT series & $C \geq 1\mu F$, within ±25% **10V: 0603 $\geq 4.7\mu F$; 0402 $\geq 1\mu F$; 0201 $\geq 0.1\mu F$, within ±25%; Y5V: $\geq 10V$, within ±30%; $\leq 6.3V$, within +30/-40% Q/D.F. value: NP0: More than 30pF, Q≥350 10pF $\leq C < 30pF$, Q≥275+2.5C Less than 10pF, Q≥200+10C X7R, X5R, X6S, X7S:																																																																																		
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1825 2220 2225	X7R	100V~250V	$C \geq 1.0\mu F$																																																																																					
		* Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * De-rating conditions:																																																																																						
																																																																																								
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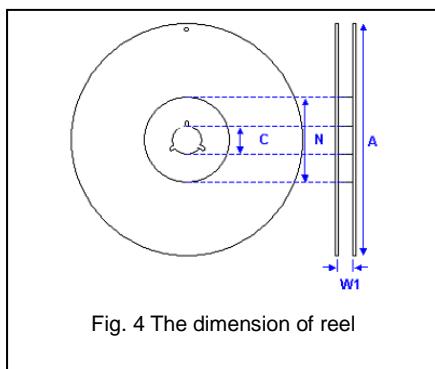
Multilayer Ceramic Capacitors

APPENDIXES

□ Tape & reel dimensions



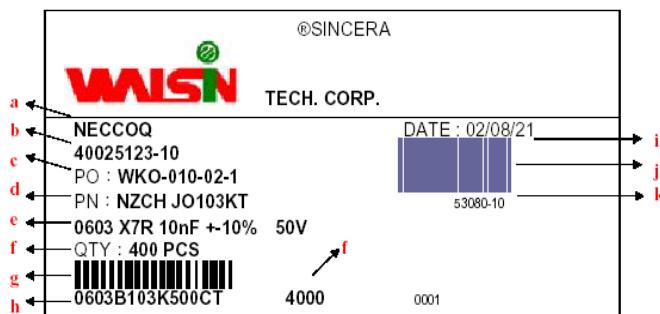
Size	0201	0402	0603	0805			1206			1210			1808	1812	
Thickness	L	N,E	S,H,X	A,H	B,T	D,I	B,T	C,J,D	G,P	T	C,D,G,K	M	D,F,G,K	M,U	
A₀	0.40 +/-0.10	0.70 +/-0.20	1.05 +/-0.30	1.50 +/-0.20	1.50 +/-0.20	< 1.80	1.90 +/-0.50	< 2.00	< 2.30	< 3.05	< 3.05	< 3.20	< 2.50	< 3.90	
B₀	0.70 +/-0.10	1.20 +/-0.20	1.80 +/-0.30	2.30 +/-0.20	2.30 +/-0.20	< 2.70	3.50 +/-0.50	< 3.70	< 4.00	< 3.80	< 3.80	< 4.00	< 5.30	< 5.30	
T	≤ 0.55	≤ 0.80	≤ 1.20	≤ 1.15	≤ 1.20	0.23 +/-0.1	≤ 1.20	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1	0.25 +/-0.1	0.25 +/-0.1	
K₀	-	-	-	-	-	< 2.50	-	< 2.50	< 2.50	< 1.50	< 2.50	< 3.20	< 2.50	< 3.50	
W	8.00 +/-0.30	12.00 +/-0.30	12.00 +/-0.30	12.00 +/-0.30											
P₀	4.00 +/-0.10														
10xP₀	40.00 +/-0.10	40.00 +/-0.10	40.00 +/-0.20												
P₁	2.00 +/-0.05	2.00 +/-0.05	4.00 +/-0.10												
P₂	2.00 +/-0.05	2.00 +/-0.10	2.00 +/-0.10	2.00 +/-0.10											
D₀	1.50 +0.1/-0														
D₁	-	-	-	-	-	1.00 +/-0.10	-	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.50 +/-0.10	1.50 +/-0.10	
E	1.75 +/-0.10														
F	3.50 +/-0.05	5.50 +/-0.10	5.50 +/-0.10												



Size	0201, 0402, 0603, 0805, 1206, 1210			1812
Reel size	7"	10"	13"	7"
C	13.0+0.5/-0.2	13.0+0.5/-0.2	13.0+0.5/-0.2	13.0+0.5/-0.2
W₁	8.4+1.5/-0	8.4+1.5/-0	8.4+1.5/-0	12.4+2.0/-0
A	178.0±1.0	250.0±1.0	330.0±1.0	178.0±1.0
N	60.0+1.0/-0	100.0±1.0	100±1.0	60.0+1.0/-0

Multilayer Ceramic Capacitors

□ Description of customer label



- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label

□ Constructions

No.	Name	NPO	X7R, X5R, X6S, X7S, Y5V
①	Ceramic material	CaZrO ₃ based	BaTiO ₃ based
②	Inner electrode	Ni	
③	Inner layer	Cu	
④	Termination	Middle layer	Ni
⑤		Outer layer	Sn

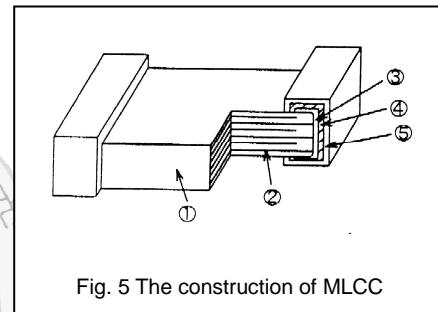


Fig. 5 The construction of MLCC

□ Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70% related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

Multilayer Ceramic Capacitors

□ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N₂ within oven are recommended.

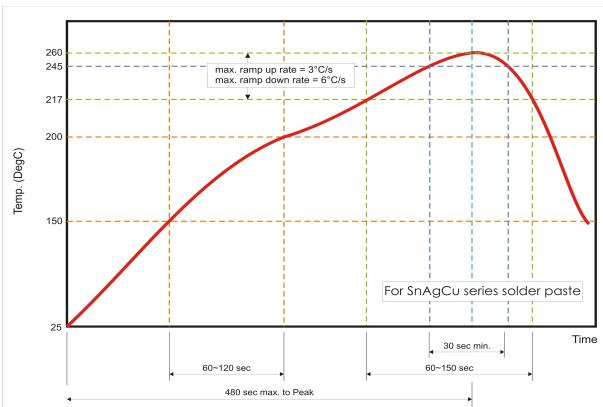


Fig. 6 Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.

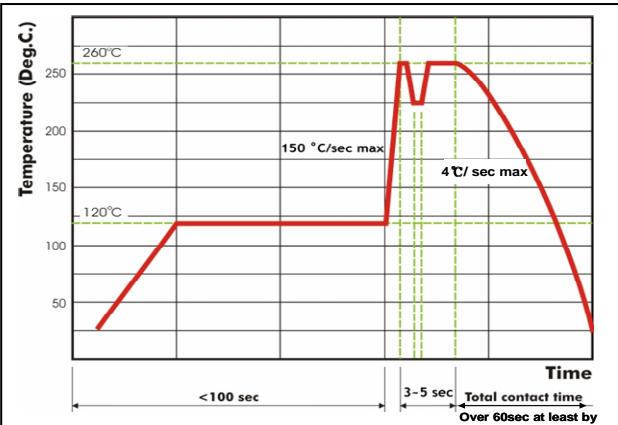


Fig. 7 Recommended wave soldering profile for SMT process with SnAgCu series solder.

