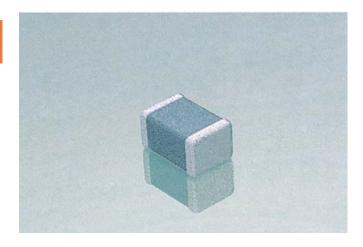
COG (NP0) Dielectric

General Specifications

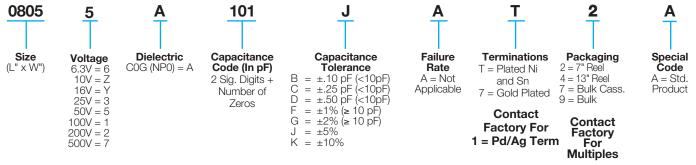




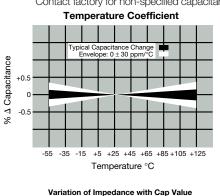
COG (NP0) is the most popular formulation of the "temperature-compensating," EIA Class I ceramic materials. Modern COG (NP0) formulations contain neodymium, samarium and other rare earth oxides.

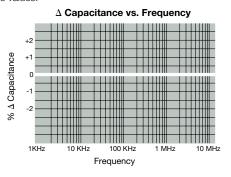
COG (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 $\pm 30 ppm/^{\circ}C$ which is less than $\pm 0.3\%$ Δ C from -55°C to +125°C. Capacitance drift or hysteresis for COG (NP0) ceramics is negligible at less than $\pm 0.05\%$ versus up to $\pm 2\%$ for films. Typical capacitance change with life is less than $\pm 0.1\%$ for COG (NP0), one-fifth that shown by most other dielectrics. COG (NP0) formulations show no aging characteristics.

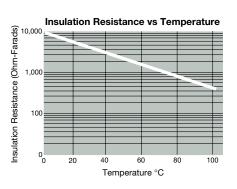
PART NUMBER (see page 2 for complete part number explanation)

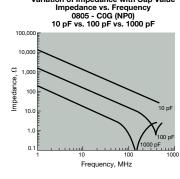


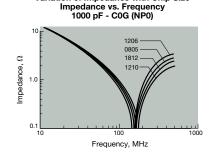
NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.



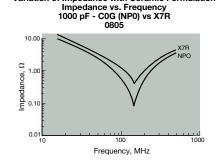








Variation of Impedance with Chip Size



Variation of Impedance with Ceramic Formulation



C0G (NP0) Dielectric



Specifications and Test Methods

Parame	ter/Test	NP0 Specification Limits	Measuring Conditions							
Operating Tem		-55°C to +125°C	Temperature Cycle Chamber							
Capac	itance	Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF							
	,	<30 pF: Q≥ 400+20 x Cap Value	1.0 kHz \pm 10% for cap > 1000 pF							
Q		≥30 pF: Q≥ 1000	Voltage: 1.0Vrms ± .2V							
Insulation Resistance		100,000M Ω or 1000M Ω - μF,	Charge device with rated voltage for							
ilisulation nesistance		whichever is less	60 ± 5 secs @ room temp/humidity							
Dielectric	Strength	No breakdown or visual defects	Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.							
	Appearance	No defects	Deflection							
Resistance to	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds 7 1mm/sec							
Flexure Stresses	Q	Meets Initial Values (As Above)	90 mm							
	Insulation Resistance	≥ Initial Value x 0.3								
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 \pm 5°C for 5.0 \pm 0.5 seconds							
Resistance to Solder Heat	Appearance	No defects, <25% leaching of either end terminal								
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.							
	Q	Meets Initial Values (As Above)								
	Insulation Resistance	Meets Initial Values (As Above)								
	Dielectric Strength	Meets Initial Values (As Above)								
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes						
Thermal Shock	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp	≤ 3 minutes						
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes						
oo	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes						
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature							
Load Life	Appearance	No visual defects								
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0). Remove from test chamber and stabilize at room temperature for 24 hours before measuring.							
	Q (C=Nominal Cap)	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C								
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)								
	Dielectric Strength	Meets Initial Values (As Above)								
Load Humidity	Appearance	No visual defects	Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.							
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater								
	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C								
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)								
	Dielectric Strength	Meets Initial Values (As Above)								



C0G (NP0) Dielectric





PREFERRED SIZES ARE SHADED

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SIZI	E I	0201			0402		0603			0805					1206							
Solder		Reflow Only		Reflow Only			Reflow Only				Re	flow/Wa	ve					v/Wave				
Packaç		All Paper		All Paper		All Paper			Paper/Embossed					Paper/Embossed								
(L) Length	MM (in.)	0.60 ± 0.03 (0.024 ± 0.001)		1.00 ± 0.10 (0.040 ± 0.004)		1.60 ± 0.15 (0.063 ± 0.006)			2.01 ± 0.20 (0.079 ± 0.008)					3.20 ± 0.20 (0.126 ± 0.008)								
(W) Width	MM (in.)			0.50 ± 0.10 (0.020 ± 0.004)		0.81 ± 0.15 (0.032 ± 0.006)			1.25 ± 0.20 (0.049 ± 0.008)					1.60 ± 0.20 (0.063 ± 0.008)								
(t) Terminal	MM	0.15 ± 0.05		0.25 ± 0.15		0.35 ± 0.15			0.50 ± 0.25					0.50 ± 0.25								
(i) FORTIFICAL	(in.) WVDC				.010 ± 0.0 25			(0.014 ± 0.006) 25 50 100		(0.020 ± 0.010) 16 25 50 100 200					(0.020 ± 0.010) 16 25 50 100 200 500							
Cap	0.5	10	10	Α	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
(pF)	1.0 1.2			A A	C	C	C	G G	G G	G G	G G	J J	J	J	J J	J J	J	J	J	J J	J J	J
	1.5	Α	Α	А	С	С	C	G	G	G	G	J	J	J	J	J	Ĵ	J	J	J	J	J
	1.8 2.2	A A	A A	A A	C	C	C	G G	G G	G G	G G	J J	J	J	J	J J	J	J	J	J	J J	J
	2.7	Α	Α	A	Ċ	C	C	G	G	G	G	J	J	J	J	J	Ĵ	J	J	Ĵ	J	J
	3.3 3.9	A A	A A	A A	C	C	C	G G	G G	G G	G G	J J	J	J	J J	J J	J J	J	J	J J	J J	J J
	4.7	A	A	A	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	5.6 6.8	A A	A A	A A	C C	C	C C	G G	G G	G G	G G	J J	J	J	J J	J J	J	J	J	J	J	J
·	8.2 10	A A	A	A	C C	C	C	G G	G G	G G	G G	J J	J	J	J	J J	J J	J	J	J J	J	J
	12	A	Α	A	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	15 18	A A	A	A	C	C	C	G G	G G	G G	G G	J J	J	J	J	J	J	J	J	J	J	J
	22	Α	Α	Α	С	С	С	G	G	G	G	J	J	J	J	J	Ĵ	J	J	J	J	J
	27 33	A A	A	A	C	C	C	G G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J
	39	Α	Α	Α	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
-	47 56	A A	A	A	C	C	C	G G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J
	68	A	A	A	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	82 100	A A	A	A	C	C	C	G G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J
	120 150				C C	C	C	G G	G G	G G	G G	J J	J	J	J J	J J	J J	J	J J	J J	J J	J
	180				С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	220 270				С	С	С	G G	G	G G	G G	J J	J	J	J	J M	J J	J	J	J	J J	M M
	330							G	G	G	G	J	J	J	J	М	J	J	J	J	J	М
	390 470							G G	G G	G G		J J	J	J	J	M M	J	J	J	J	J J	M M
	560 680							G G	G G	G G		J J	J J	J	J J	М	J	J	J	J J	J	M P
	820							G	G	G		J	J	J	J		J	J	J	J	J M	Г
	1000 1200							G	G	G		J J	J	J	J		J	J	J	J J	QQ	
	1500											Ĵ	J	J			Ĵ	J	J	М	Q	
	1800 2200											J J	J	J M			J J	J	M M	M P		
	2700											J	J	М			J	J	M	P P		
	3300 3900			I	I	l	I										J	J	M M	Р		
	4700 5600			, >	<u>~</u>	€ W-	•						-				J	J	M	Р		
	6800			كِّ ﴿	_	$\int \int_{\mathbb{R}}$	ÎT										М	М				
Cap	8200 0.010]	السل	<u> </u>	-					-				M M	M				
(µF)	0.012			_	العبدا																	
	0.015 0.018				[
	0.022 0.027																					
	0.033																					
	0.039 0.047																					
	0.068																					
	0.082 0.1									L			L	L			L	L				
	WVDC	10	16	25	16	25	50	6.3	25	50	100	16	25	50	100	200	16	25	50	100	200	500
	SIZE		0201	_		0402			06			<u> </u>		0805	.,		<u> </u>	_		.06		
Letter Max.	A 0.33	0.8		E 0.71	G		J	1.02		M .27	N	1.5		Q 1.78	X 2.29		Y	Z				
Thickness	(0.013)	(0.0))22)	(0.028)	(0.03		0.037)	(0.040		050)	(0.055)	(0.0)	60) (0.070)	(0.090		.100)	(0.11				
		PAPER EMBOSSED																				



C0G (NP0) Dielectric



Capacitance Range

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